## SIEMENS

Data sheet

***Spare part*** SIMATIC S7-300, CPU 312C Compact CPU with MPI, 10 DI/6 DQ, 2 high-speed counters ( 10 kHz ) Integr. power supply 24 VDC , Work memory 32 KB, Front connector (1x 40-pole) and Micro Memory Card required

Figure similar

| General information |  |
| :---: | :---: |
| HW functional status | 01 |
| Firmware version | V2.6 |
| Engineering with |  |
| - Programming package | STEP 7 V 5.3 SP2 or higher with HW update |
| Supply voltage |  |
| Rated value (DC) | 24 V |
| permissible range, lower limit (DC) | 20.4 V |
| permissible range, upper limit (DC) | 28.8 V |
| external protection for power supply lines (recommendation) | Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A |
| Load voltage L+ |  |
| - Rated value (DC) |  |
| - permissible range, lower limit (DC) | 20.4 V |
| - permissible range, upper limit (DC) | 28.8 V |
| Digital inputs |  |
| - Rated value (DC) | 24 V |
| - Reverse polarity protection | Yes |
| Digital outputs |  |
| - Rated value (DC) | 24 V |
| - Reverse polarity protection | No |
| Input current |  |
| Current consumption (rated value) | 500 mA |
| Current consumption (in no-load operation), typ. | 60 mA |
| Inrush current, typ. | 11 A |
| $1^{2} \mathrm{t}$ | $0.7 \mathrm{~A}^{2}$. s |
| Digital outputs |  |
| - from load voltage L+, max. | 50 mA |
| Power loss |  |
| Power loss, typ. | 6 W |
| Memory |  |
| Work memory |  |
| - integrated | 32 kbyte |
| - expandable | No |
| Load memory |  |
| - Plug-in (MMC) | Yes |
| - Plug-in (MMC), max. | 4 Mbyte |
| - Data management on MMC (after last | 10 y |

programming), min.
Backup

| - present | Yes; Guaranteed by MMC (maintenance-free) |
| :--- | :--- |
| $\bullet$ - without battery | Yes; Program and data |

CPU processing times

| for bit operations, typ. | $0.2 \mu \mathrm{~s}$ |
| :--- | :--- |
| for bit operations, max. | $0.4 \mu \mathrm{~s}$ |
| for word operations, typ. | $0.4 \mu \mathrm{~s}$ |
| for fixed point arithmetic, typ. | $5 \mu \mathrm{~s}$ |
| for floating point arithmetic, typ. | $6 \mu \mathrm{~s}$ |

CPU-blocks
Number of blocks (total)
1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.

```
DB
```

- Number, max.
- Size, max.

FB

- Number, max.
- Size, max.

16 kbyte
FC

- Number, max.
- Size, max.

1 024; Number range: 0 to 2047
16 kbyte

## OB

- Number, max.
- Size, max.
- Number of free cycle OBs
- Number of time alarm OBs
- Number of delay alarm OBs
- Number of cyclic interrupt OBs
- Number of process alarm OBs
- Number of startup OBs
- Number of asynchronous error OBs
- Number of synchronous error OBs
see instruction list
16 kbyte
1; OB 1
1; OB 10
1; OB 20
1; OB 35
1; OB 40
1; OB 100
4; OB 80, 82, 85, 87
2; OB 121, 122
Nesting depth
- per priority class 8
- additional within an error OB

4
Counters, timers and their retentivity
S7 counter

- Number

128
Retentivity

| — adjustable | Yes |
| :--- | :--- |
| — lower limit | 0 |

— upper limit 127
— preset 8

| Counting range | 0 |
| ---: | :--- |
| — lower limit | 99 |

IEC counter

- present Yes
- Type
- Number

SFB
Unlimited (limited only by RAM capacity)

## S7 times

- Number

128
Retentivity

| — adjustable | Yes |
| :--- | :--- |
| - lower limit | 0 |
| — upper limit | 127 |

- upper limit 127
— preset
No retentivity
Time range
— lower limit

| - upper limit | 9990 s |
| :---: | :---: |
| IEC timer |  |
| - present | Yes |
| - Type | SFB |
| - Number | Unlimited (limited only by RAM capacity) |
| Data areas and their retentivity |  |
| Retentive data area (incl. timers, counters, flags), max. | 32 kbyte |
| Flag |  |
| - Size, max. | 128 byte |
| - Retentivity available | Yes; MB 0 to MB 127 |
| - Retentivity preset | MB 0 to MB 15 |
| - Number of clock memories | 8; 1 memory byte |
| Data blocks |  |
| - Retentivity adjustable | Yes; via non-retain property on DB |
| - Retentivity preset | Yes |
| Local data |  |
| - per priority class, max. | 256 byte |
| Address area |  |
| I/O address area |  |
| - Inputs | 1 kbyte |
| - Outputs | 1 kbyte |
| Process image |  |
| - Inputs | 128 byte |
| - Outputs | 128 byte |
| Default addresses of the integrated channels |  |
| - Digital inputs | 124.0 to 125.1 |
| - Digital outputs | 124.0 to 124.5 |
| Digital channels |  |
| - Inputs | 266 |
| - of which central | 266 |
| - Outputs | 262 |
| - of which central | 262 |
| Analog channels |  |
| - Inputs | 64 |
| - of which central | 64 |
| - Outputs | 64 |
| - of which central | 64 |
| Hardware configuration |  |
| Number of expansion units, max. | 0 |
| Number of DP masters |  |
| - integrated | none |
| - via CP | 4 |
| Number of operable FMs and CPs (recommended) |  |
| - FM | 8 |
| - CP, PtP | 8 |
| - CP, LAN | 4 |
| Rack |  |
| - Racks, max. | 1 |
| - Modules per rack, max. | 8 |
| Time of day |  |
| Clock |  |
| - Software clock | Yes |
| - retentive and synchronizable | No |
| - Deviation per day, max. | 15 s |
| Operating hours counter |  |
| - Number | 1 |
| - Number/Number range | 0 |
| - Range of values | 0 to $2^{\wedge} 31$ hours (when using SFC 101) |
| - Granularity | 1 h |

- retentive


## Yes; Must be restarted at each restart

Clock synchronization

- supported

Yes

- to MPI, master
- to MPI, slave
- in AS, master

Yes
Yes
Digital inputs
Number of digital inputs 10

- of which inputs usable for technological functions 8

| integrated channels (DI) | 10 |
| :--- | :--- |

Input characteristic curve in accordance with IEC 61131, Yes type 1
Number of simultaneously controllable inputs
horizontal installation

$$
\begin{array}{ll}
\text { - up to } 40^{\circ} \mathrm{C} \text {, max. } & 10 \\
\text { - up to } 60^{\circ} \mathrm{C} \text {, max. } & 5 \\
\hline
\end{array}
$$vertical installation—up to $40^{\circ} \mathrm{C}$, max. 5

Input voltage

- Rated value (DC) 24 V
- for signal "0"
- for signal "1"
-3 to +5 V
+15 to +30 V
Input current
- for signal "1", typ.

9 mA
Input delay (for rated value of input voltage)
for standard inputs

$$
\begin{array}{c|c}
\hline \text { — parameterizable } & \text { Yes; } 0 \\
\text { — Rated value } & 3 \mathrm{~ms} \\
\text { for technological functions } & 48 \mu \mathrm{~s} \\
\hline \text { — at "0" to "1", max. } &
\end{array}
$$

Yes; 0.1 / 0.3 / 3 / 15 ms

Cable length

- shielded, max.
- unshielded, max.
for technological functions

$$
\begin{aligned}
& \text { — shielded, max. } \\
& \text { — unshielded, max. }
\end{aligned}
$$

$1000 \mathrm{~m} ; 100 \mathrm{~m}$ for technological functions
600 m ; for technological functions: No

100 m
not allowed

Digital outputs
Number of digital outputs 6

- of which high-speed outputs 2
integrated channels (DO)

Short-circuit protection

- Response threshold, typ.

Limitation of inductive shutdown voltage to
Controlling a digital input

+ (-48 V)

Switching capacity of the outputs

- on lamp load, max. 5 W

Load resistance range

- lower limit
$48 \Omega$
- upper limit
$4 \mathrm{k} \Omega$
Output voltage
- for signal "1", min

L+ (-0.8 V)
Output current

- for signal "1" rated value

500 mA

- for signal "1" permissible range, min.
- for signal "1" permissible range, max.
- for signal "1" minimum load current
- for signal "0" residual current, max.

5 mA
0.6 A

5 mA
0.5 mA

Parallel switching of two outputs

- for uprating
- for redundant control of a load

| Switching frequency |  |
| :---: | :---: |
| - with resistive load, max. <br> - with inductive load, max. <br> - on lamp load, max. <br> - of the pulse outputs, with resistive load, max. | $\begin{aligned} & 100 \mathrm{~Hz} \\ & 0.5 \mathrm{~Hz} \\ & 100 \mathrm{~Hz} \\ & 2.5 \mathrm{kHz} \end{aligned}$ |
| Total current of the outputs (per group) |  |
| horizontal installation |  |
| - up to $40^{\circ} \mathrm{C}$, max. <br> - up to $60^{\circ} \mathrm{C}$, max. | $\begin{aligned} & 2 \mathrm{~A} \\ & 1.5 \mathrm{~A} \end{aligned}$ |
| vertical installation |  |
| - up to $40{ }^{\circ} \mathrm{C}$, max. | 1.5 A |
| Cable length |  |
| - shielded, max. <br> - unshielded, max. | $\begin{aligned} & 1000 \mathrm{~m} \\ & 600 \mathrm{~m} \end{aligned}$ |
| Analog inputs |  |
| integrated channels (AI) | none |
| Analog outputs |  |
| integrated channels (AO) | none |
| Encoder |  |
| Connectable encoders |  |
| - 2-wire sensor <br> - permissible quiescent current (2-wire sensor), max. | Yes <br> 1.5 mA |
| Interfaces |  |
| Number of industrial Ethernet interfaces | 0 |
| Number of PROFINET interfaces | 0 |
| Number of RS 485 interfaces | 1; MPI |
| Number of RS 422 interfaces | 0 |
| MPI |  |
| - Cable length, max. | 50 m ; without repeater |
| 1. Interface |  |
| Interface type | Integrated RS 485 interface |
| Isolated | No |
| Interface types |  |
| - RS 485 | Yes |
| - Output current of the interface, max. | 200 mA |
| Protocols |  |
| - MPI <br> - PROFIBUS DP master <br> - PROFIBUS DP slave <br> - Point-to-point connection | Yes <br> No <br> No <br> No |
| MPI |  |
| - Number of connections <br> - Transmission rate, max. | $\begin{aligned} & 6 \\ & 187.5 \mathrm{kbit} / \mathrm{s} \end{aligned}$ |
| Services |  |
| - PG/OP communication <br> — Routing <br> - Global data communication <br> - S7 basic communication <br> - S7 communication <br> - S7 communication, as client <br> - S7 communication, as server | Yes <br> No <br> Yes <br> Yes <br> Yes <br> No <br> Yes |
| Protocols |  |
| PROFIsafe | No |
| communication functions / header |  |
| PG/OP communication | Yes |
| Global data communication |  |
| - supported <br> - Number of GD loops, max. | Yes $4$ |

- Number of GD packets, max.
- Number of GD packets, transmitter, max.
- Number of GD packets, receiver, max.
- Size of GD packets, max.
- Size of GD packet (of which consistent), max.

57 basic communication

- supported
- User data per job, max.
- User data per job (of which consistent), max.

4
4
4
22 byte
22 byte

## S7 communication

- supported


## Yes

## 76 byte

76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)

- as server
- as client
- User data per job, max.
- User data per job (of which consistent), max.


## Yes

Yes
Yes; Via CP and loadable FB
180 byte; With PUT/GET
64 byte
S5 compatible communication

- supported Yes; via CP and loadable FC

Number of connections

- overall 6
- usable for PG communication
- reserved for PG communication
- adjustable for PG communication, min.
— adjustable for PG communication, max.
- usable for OP communication
- reserved for OP communication
- adjustable for OP communication, min.
- adjustable for OP communication, max.
- usable for S 7 basic communication
- reserved for $\mathrm{S7}$ basic communication
- adjustable for S 7 basic communication, min.
- adjustable for S 7 basic communication, max.
- usable for routing


## 57 message functions

Number of login stations for message functions, max.
Process diagnostic messages Yes
simultaneously active Alarm-S blocks, max. 20
Test commissioning functions

| Status block | Yes |
| :---: | :---: |
| Single step | Yes |
| Number of breakpoints | 2 |
| Status/control |  |
| - Status/control variable <br> - Variables <br> - Number of variables, max. <br> - of which status variables, max. <br> - of which control variables, max. | Yes <br> Inputs, outputs, memory bits, DB, times, counters <br> 30 <br> 30 <br> 14 |
| Forcing |  |
| - Forcing <br> - Forcing, variables <br> - Number of variables, max. | Yes <br> Inputs, outputs $10$ |
| Diagnostic buffer |  |
| - present <br> - Number of entries, max. | $\begin{aligned} & \text { Yes } \\ & 100 \end{aligned}$ |
| Interrupts/diagnostics/status information |  |
| Diagnostics indication LED |  |
| - Status indicator digital input (green) <br> - Status indicator digital output (green) | $\begin{aligned} & \text { Yes } \\ & \text { Yes } \end{aligned}$ |


| Frequency measurement | Yes |
| :---: | :---: |
| - Number of frequency meters | 2; 2 channels up to max. 10 kHz (see "Technological Functions" manual) |
| controlled positioning | No |
| integrated function blocks (closed-loop control) | No |
| PID controller | No |
| Number of pulse outputs | 2; 2 channels pulse width modulation up to 2.5 kHz (see Manual "Technological Functions") |
| Limit frequency (pulse) | 2.5 kHz |
| Potential separation |  |
| Potential separation digital inputs |  |
| - Potential separation digital inputs <br> - between the channels <br> - between the channels and backplane bus | Yes <br> No <br> Yes |
| Potential separation digital outputs |  |
| - Potential separation digital outputs <br> - between the channels <br> - between the channels and backplane bus | Yes <br> No <br> Yes |
| Isolation |  |
| Isolation tested with | 600 V DC |
| configuration / header |  |
| Configuration software |  |
| - STEP 7 | Yes; V5.3 SP2 with HW update |
| configuration / programming / header |  |
| - Command set <br> - Nesting levels <br> - System functions (SFC) <br> - System function blocks (SFB) | see instruction list 8 <br> see instruction list see instruction list |
| Programming language |  |
| — LAD <br> - FBD <br> - STL <br> - SCL <br> - GRAPH <br> - HiGraph® | Yes <br> Yes <br> Yes <br> Yes <br> Yes <br> Yes |
| Know-how protection |  |
| - User program protection/password protection | Yes |
| Dimensions |  |
| Width | 80 mm |
| Height | 125 mm |
| Depth | 130 mm |
| Weights |  |
| Weight, approx. | 409 g |
| last modified: | 7/28/2021 |

